

HON et al.  
DRIVING METHOD OF PLASMA DISPLAY  
PANEL  
Filed: June 19, 2001  
Darryl Mexic  
1 of 15

Q64974  
202-293-7060

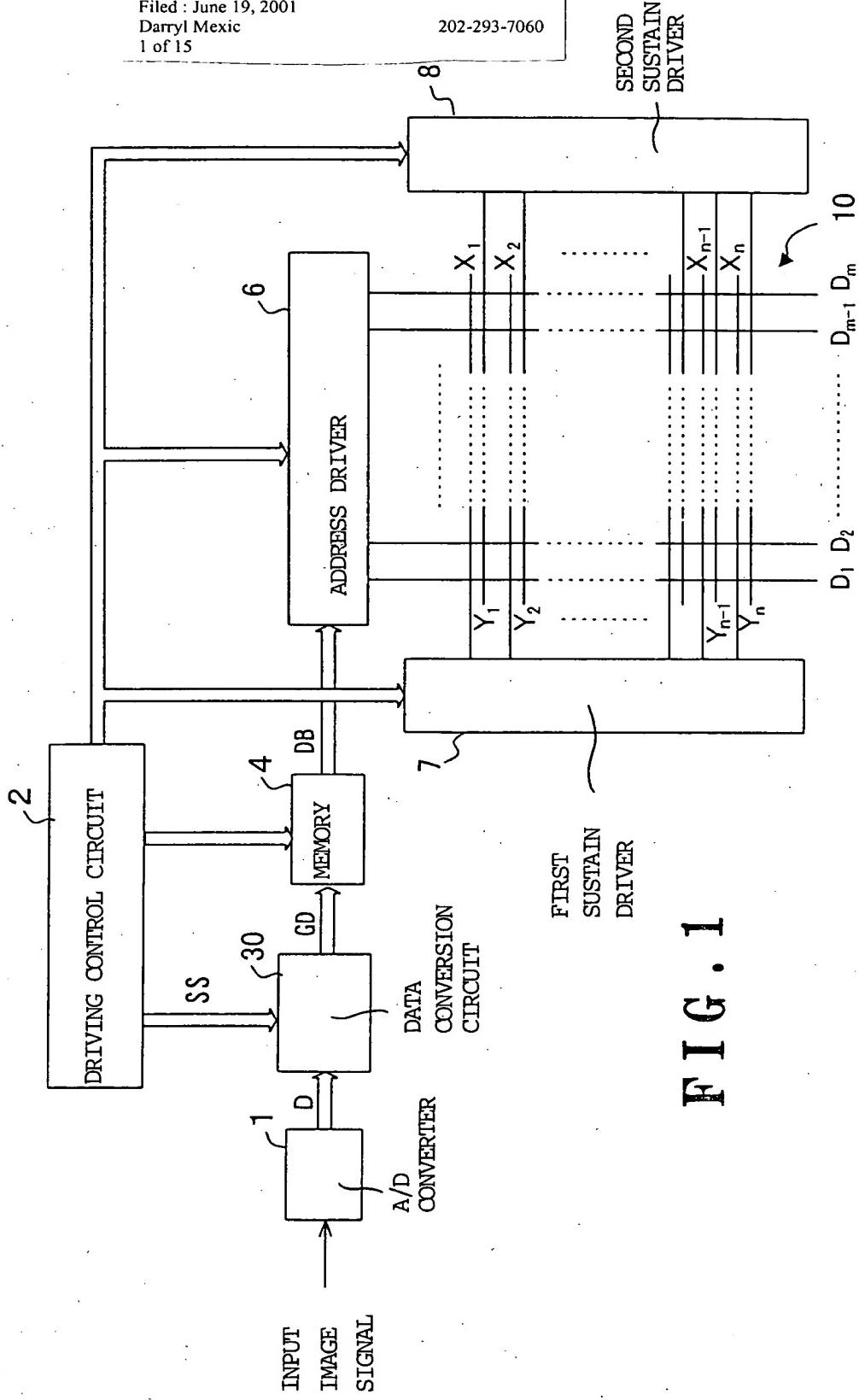


FIG. 1

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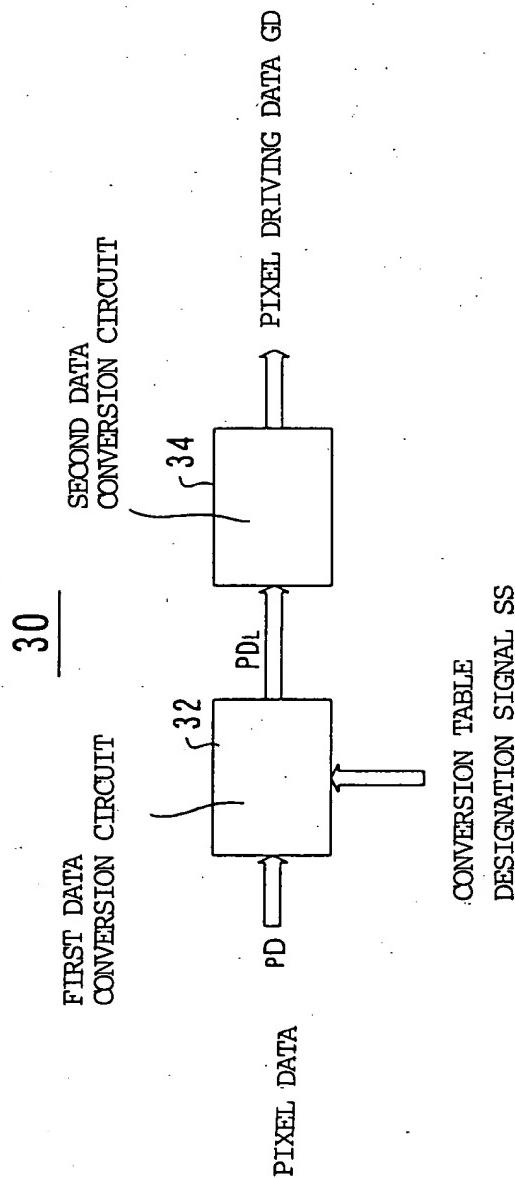


FIG. 2

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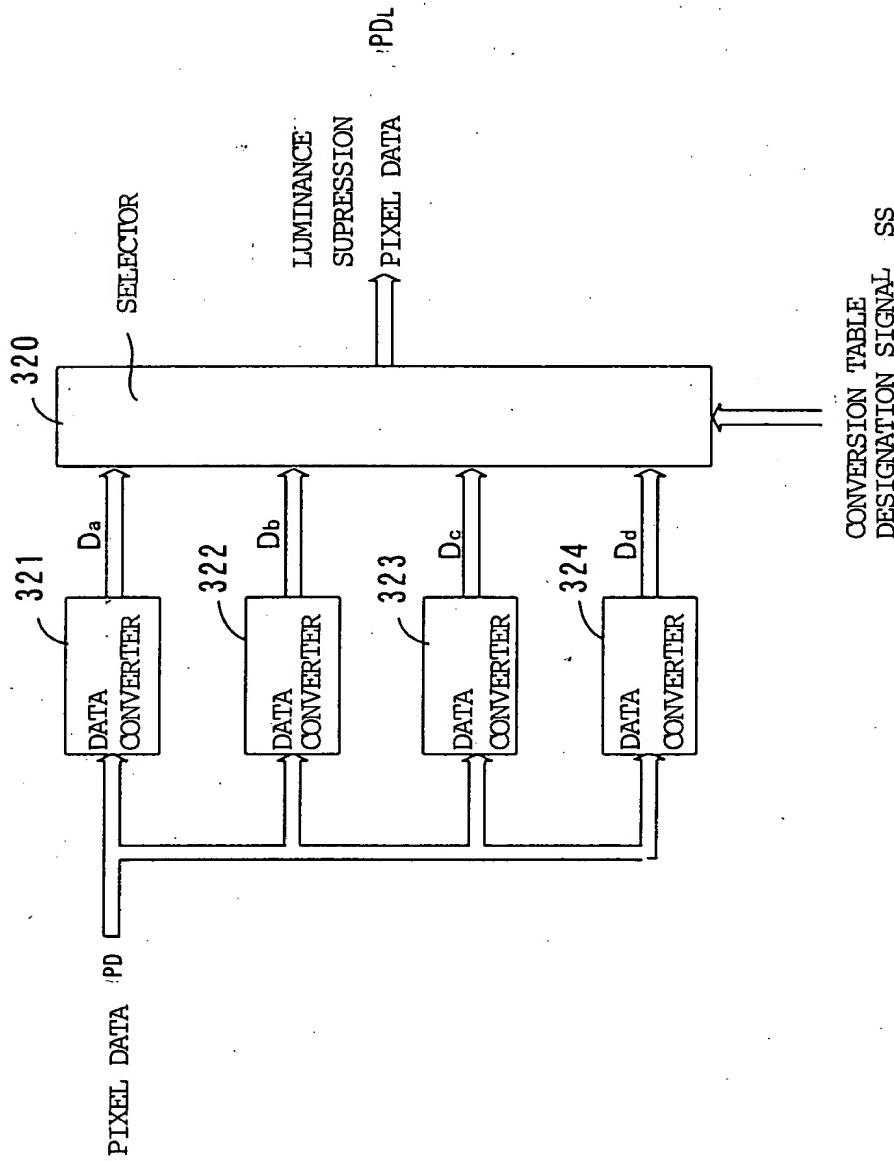


FIG. 3

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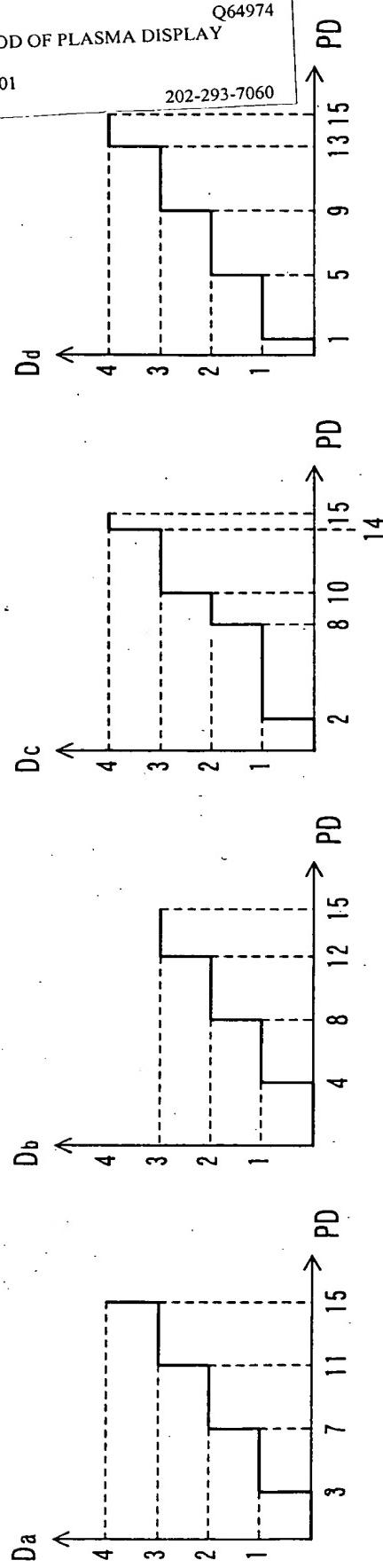
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FIG. 4 A

FIG. 4 B

FIG. 4 C

FIG. 4 D

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CONVERSION TABLE OF  
SECOND DATA CONVERSION  
CIRCUIT 34

[SELECTIVE ERASE]

LIGHT EMISSION  
DRIVING PATTERN

GRADATION	PD <sub>L</sub>	GD	SF				SF				LUMINANCE			
			1	2	3	4	1	2	3	4	A	B	C	D
1	000	1	1	1	1	1	●	●	●	●	0	0	0	0
2	001	0	1	1	1	1	○	●	●	●	20	28	12	4
3	010	0	0	1	1	1	○	○	●	●	72	88	56	40
4	011	0	0	0	1	1	○	○	○	●	156	180	132	108
5	100	0	0	0	0	0	○	○	○	○	272	240	208	

BLACK CIRCLE:SELECTIVE ERASE DISCHARGE (LIGHT, NON-EMISSION)

WHITE CIRCLE:SUSTAIN DISCHARGE(LIGHT EMISSION)

FIG. 5

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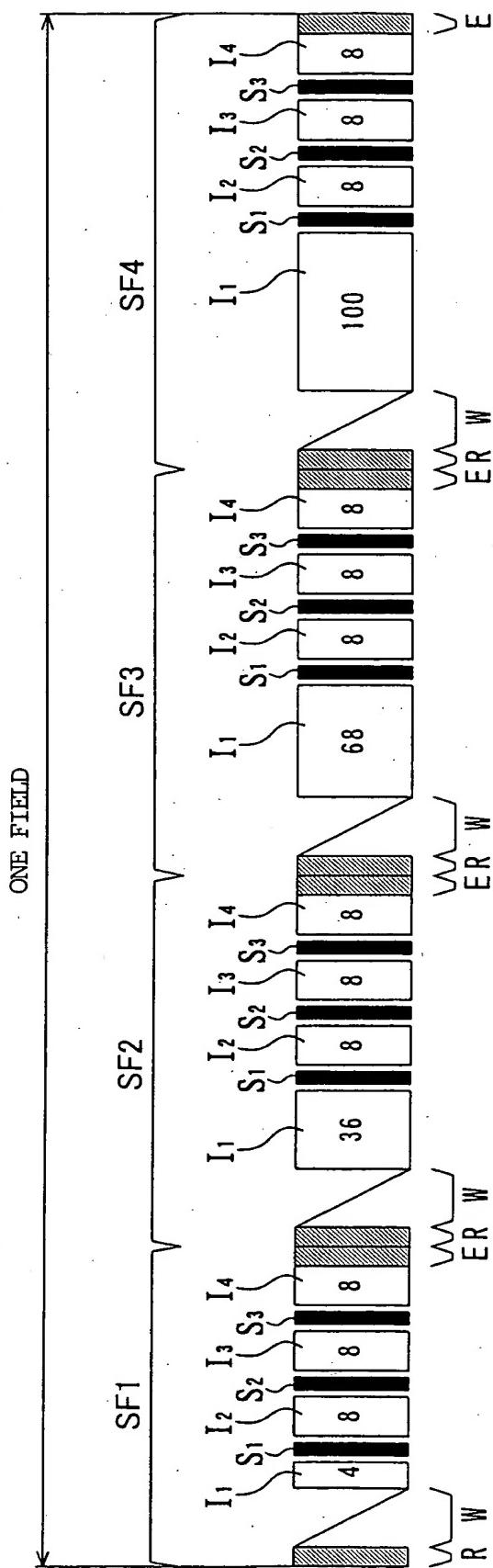
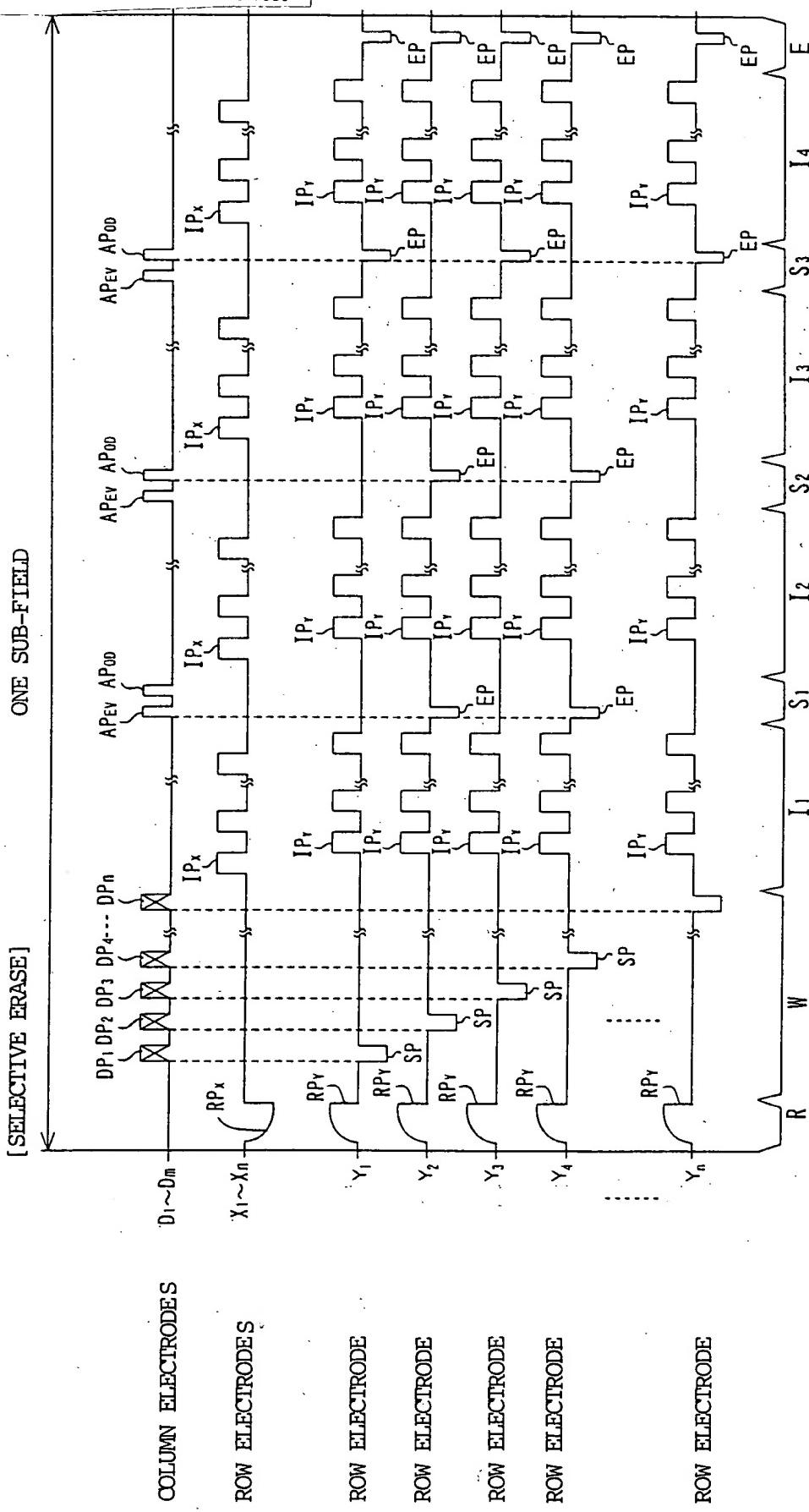
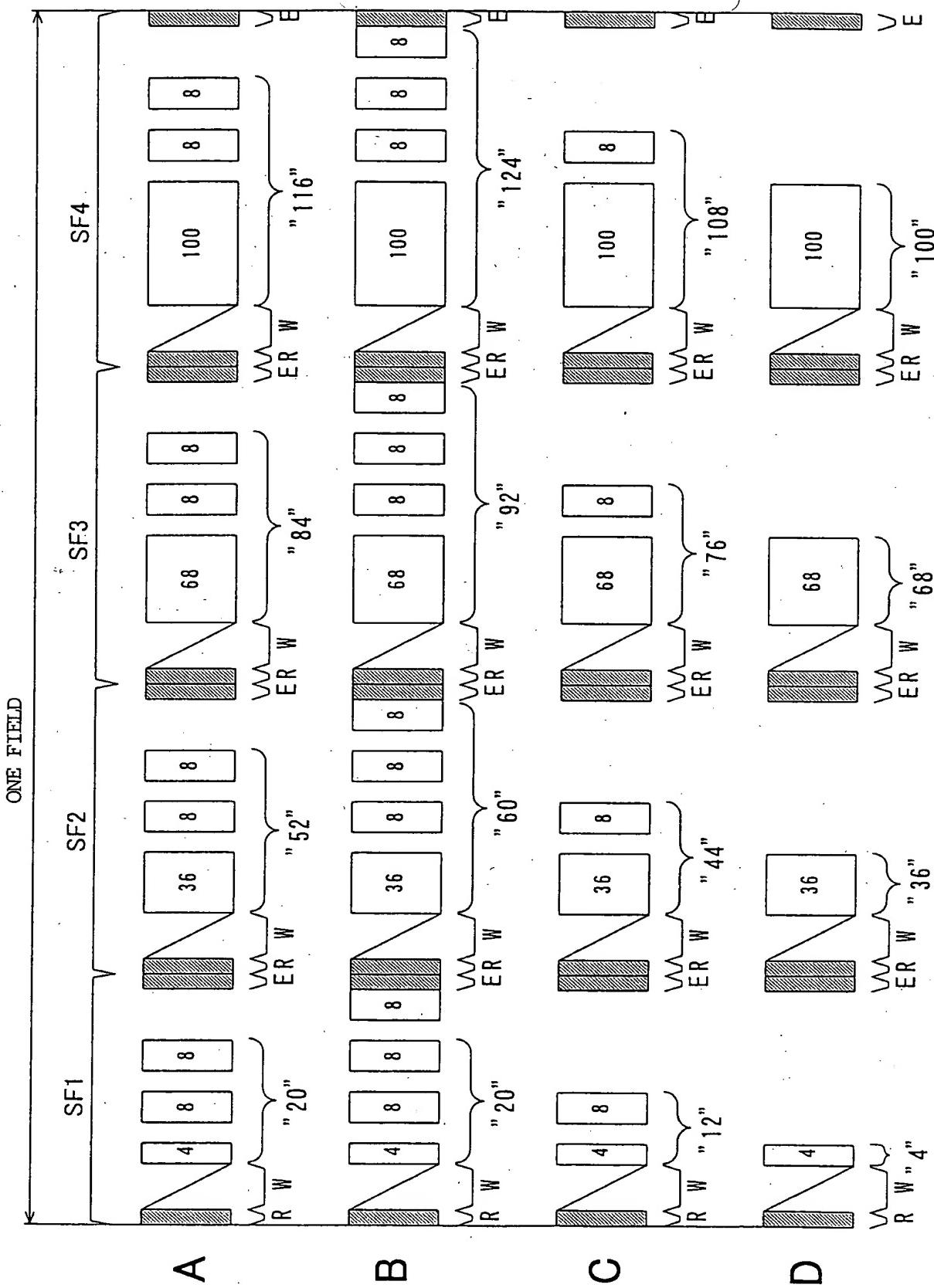


FIG. 6



7.  
•  
FIG.  
E

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1 2 3 4 .....(COLUMN)

1	A	B	A	B
2	C	D	C	D
3	A	B	A	B
4	C	D	C	D

(ROW)

FIG. 9

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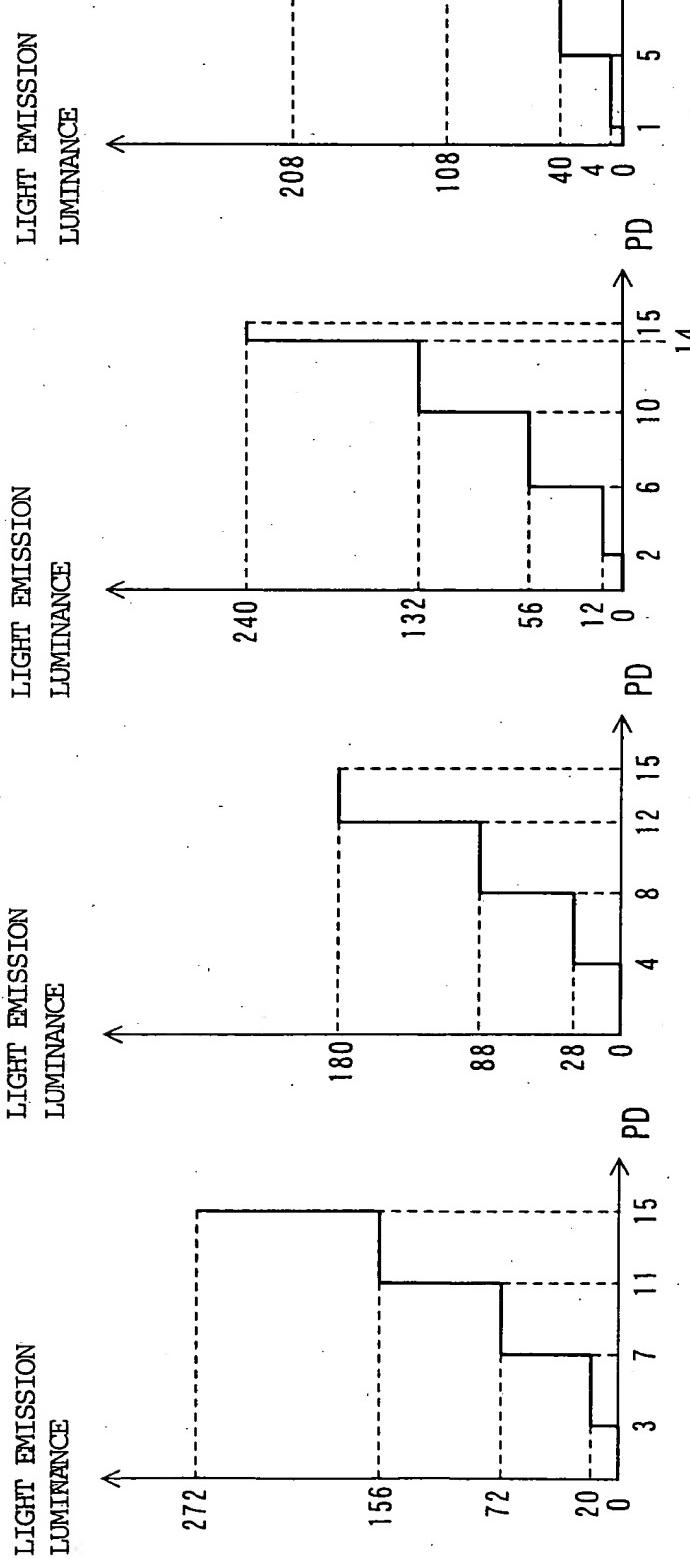
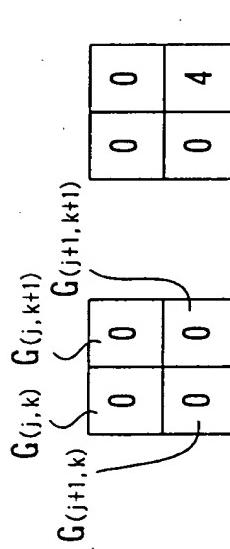


FIG . 10 A   FIG . 10 B

FIG . 10 C

FIG . 10 D

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PIXEL DATA

 $\Rightarrow$  0DISCHARGE CELL  
BLOCK LUMINANCE

0	0
0	0

 $\Rightarrow$  1 $\Rightarrow$  1

20	0
12	4

 $\Rightarrow$  2 $\Rightarrow$  4

20	28
12	4

 $\Rightarrow$  5 $\Rightarrow$  16

20	28
12	4

20	28
12	4

20	0
12	4

20	0
12	4

72	88
56	108

72	28
56	40

20	28
56	40

156	88
132	108

20	28
12	4

20	0
12	4

20	0
12	4

72	88
56	108

72	28
56	40

156	88
132	108

20	28
12	4

20	0
12	4

20	0
12	4

72	88
56	108

72	28
56	40

156	88
132	108

FIG. 11

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LIGHT EMISSION  
LUMINANCE

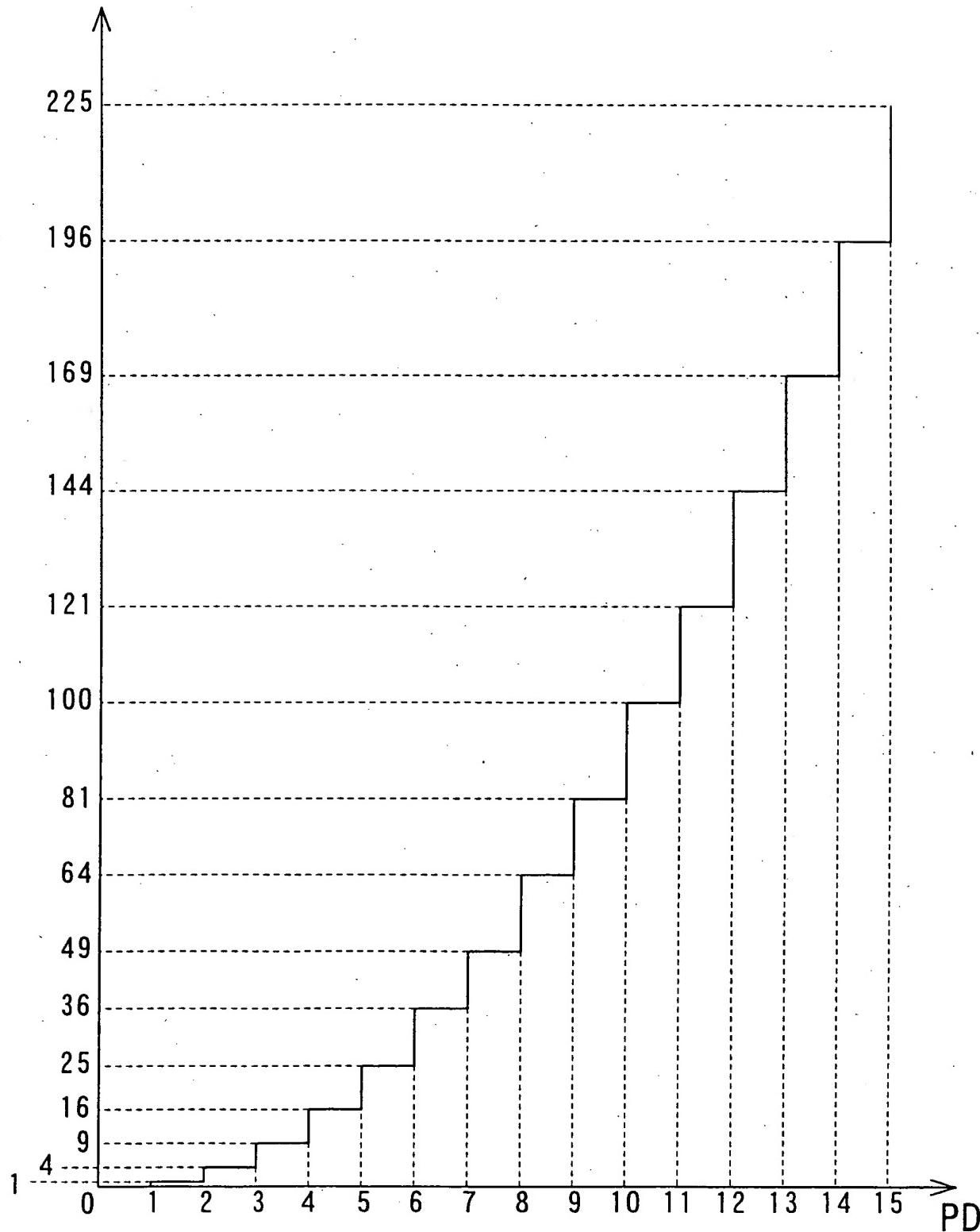


FIG. 12

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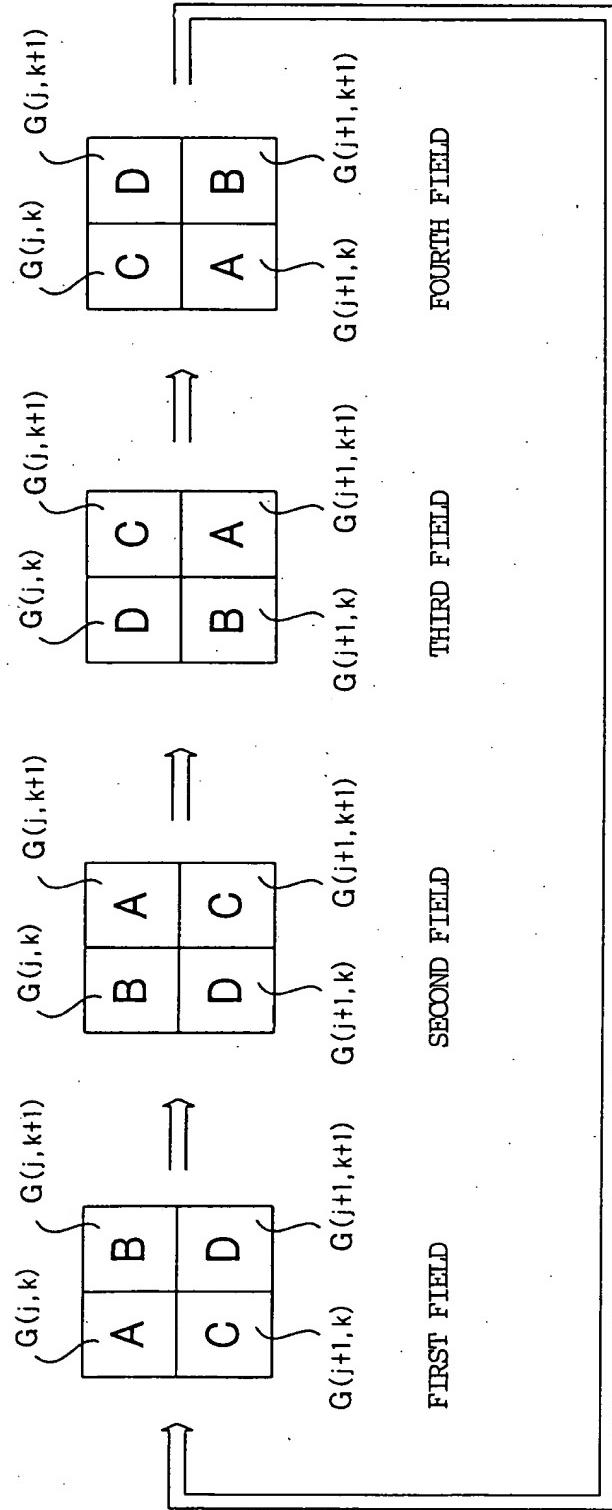


FIG. 13

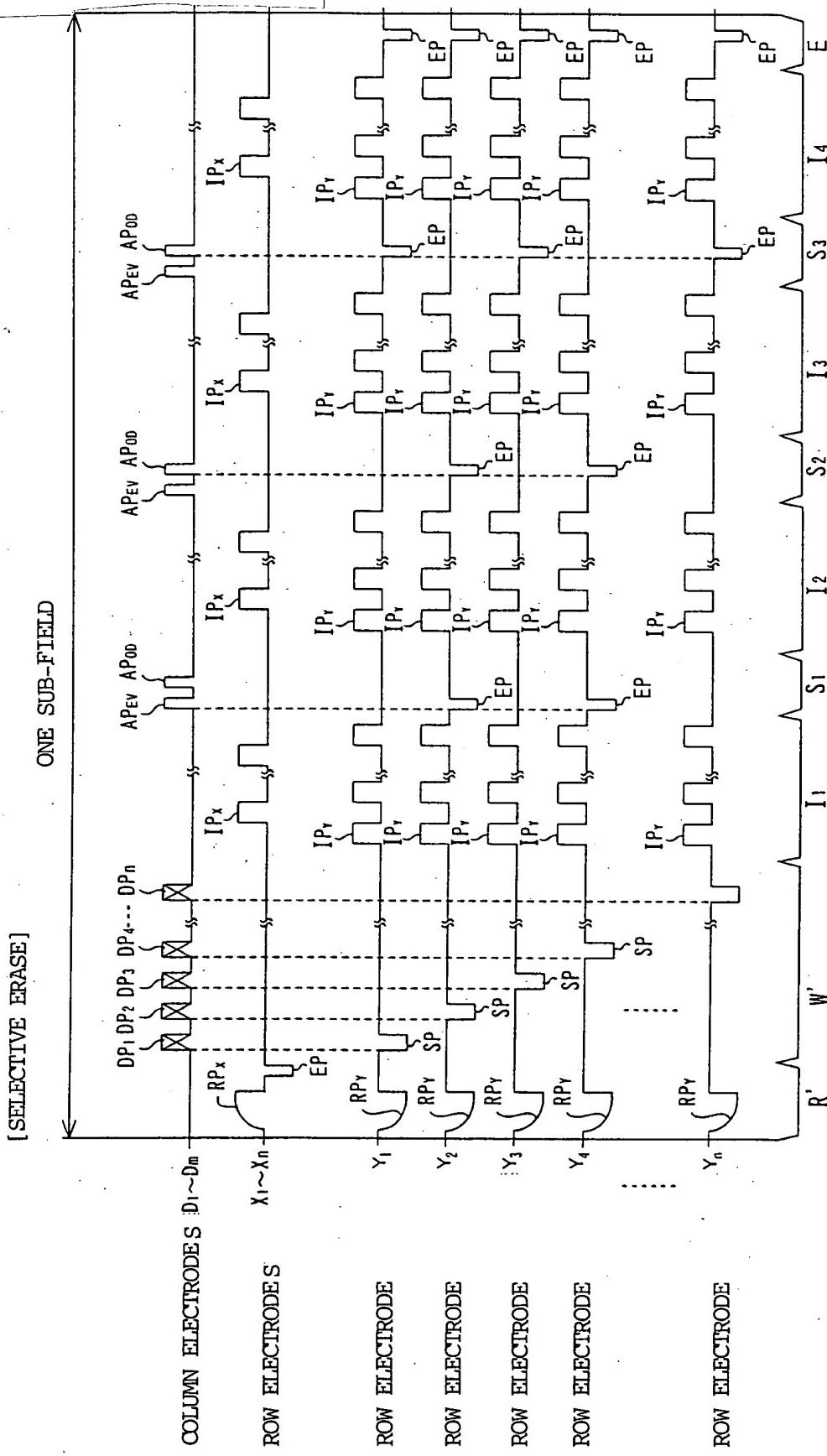


FIG . 14

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GRADATION	PD <sub>L</sub>	LIGHT EMISSION				LUMINANCE			
		GD 1 2 3 4	SF 1 2 3 4	SF A	SF B	SF C	SF D		
1	000	0 0 0 0	0 0 0 0	0	0	0	0	0	0
2	001	1 0 0 0	0 0 0 0	20	28	12	4		
3	010	1 1 0 0	0 0 0 0	72	88	56	40		
4	011	1 1 1 0	0 0 0 0	156	180	132	108		
5	100	1 1 1 1	0 0 0 0	272	240	208			

[SELECTIVE WRITE]

FIG . 15